

JT12 Rec'd PCT/PTO 10 DEC 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
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Hirofumi Totsuka et al.)	Group Art Unit: Unassigned
)	
Application No.: Unassigned)	Examiner: Unassigned
)	
Filed: December 10, 2004)	Confirmation No.: Unassigned
)	
For: CLOCK DATA RECOVERY CIRCUIT)	
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)	
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)	

FIRST INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, the accompanying information is being submitted in accordance with 37 C.F.R. §§ 1.97 and 1.98.

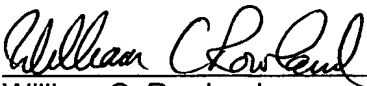
To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date December 10, 2004

By:



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Substitute for form 1449A/PTO & 1449B/PTO

Complete if Known

FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Application Number	Unassigned
Filing Date	December 10, 2004
First Named Inventor	Hirofumi Totsuka et al.
Examiner Name	Unassigned
Attorney Docket Number	032404-082

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Issue/Publication Date (MM-DD-YYYY)
	6,154,511	A	Nakamura et al.	11-28-2000
	2001/0010670	A1	Jinbo et al.	08-02-2001
	6,236,696	B1	Aoki et al.	05-22-2001

FOREIGN PATENT DOCUMENTS

Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	STATUS						
					Translation	Partial Translation	Eng. Lang. Summary	Search Report	IPER	Abstract	Cited in Spec
	*JP 10-145348	A	Japan	05-29-1998				*			
	*JP 2001-210020	A	Japan	08-03-2001				*			
	*JP 61-114628	A	Japan	06-02-1986			*	*			
	*JP 10-327068	A	Japan	12-08-1998				*			
	*JP 61-171320	U	Japan	10-24-1986			*	*			

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	*ANSGAR POTTBACKER ET AL., "A Si Bipolar Phase and Frequency Detector IC for Clock Extraction up to 8GB/s", IEEE Journal of Solid State Circuits, December 1992, Vol. SC-27, pp. 1747-1751.

*Copy Attached

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.